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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		09989762
	Filing Date		2001-11-19
	First Named Inventor	Kenneth Y. Ogami	
	Art Unit	2123	
	Examiner Name	Osborne, Luke R.	
Attorney Docket Number		CYPY-CD01175M	

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	4	6592626		2003-07-15	Bauchot , et al.	
	5	6611952		2003-08-26	Prakash , et al.	
	6	6613098		2003-09-02	Sorge , et al.	
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9	6715132		2004-03-30	Bartz , et al.	
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	2	20020188910	A1	2002-12-12	Zizzo, Claudio	
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2	C. Ebeling et al.; "VALIDATING VLSI CIRCUIT LAYOUT BY WIRELIST COMPARISON"; Sept. 1983; In proceedings of the IEEE International Conference on Computer Aided Design (ICCAD-83), pp. 172-173	<input type="checkbox"/>
3	"THE GEMINI NETLIST COMPARISON PROJECT"; <a href="http://www.cs.washington.edu/research/projects/lis/www/gemini/gemini.html">http://www.cs.washington.edu/research/projects/lis/www/gemini/gemini.html</a> , larry@cs.washington.edu	<input type="checkbox"/>
4	Ohlrich et al.; "SUB-GEMINI: IDENTIFYING SUBCIRCUITS USING A FAST SUBGRAPH ISOMORPHISM ALGORITHM"; June 1993; In proceedings of the 30th IEEE/ACM Design Automation Conference 7 pages	<input type="checkbox"/>
5	Ebeling; "GEMINI II: A SECOND GENERATION LAYOUT VALIDATION PROGRAM"; 1988; In proceedings of the IEEE International Conference on Computer Aided Design (ICCAD-88), pp. 322-325	<input type="checkbox"/>
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